

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Docket No.: 884.895US1

Filed: September 29, 2000

Examiner: Thu-Thao Havan

Customer No.: 21186

Serial No.: 09/672,954

Due Date: May 18, 2005

Group Art Unit: 2672

Confirmation No.: 2928

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Notice of Allowance Date:

February 18, 2005

We are transmitting herewith the attached:

X Communication re: Submission of Formal Drawings (1 pg.)

X 4 pages of Formal Drawings

X A copy of Issue Fee payment documents (PTOL-85, checks, Comments on Statements for Reasons for Allowance, Amendment under 37 C.F.R. § 1.312(1)), submitted under separate cover (18 pgs.)

X A return postcard.

Please charge any additional required fees for the Issue Fee Payment or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

By Jan M. Mc Ceach

Ann M. McCrackin Reg. No. 42,858

AMM1:CMG:jdgw

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ANN MCCRACKIN

Name

Signature

OIPE N 09/672,954

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Bimal Poddar

Examiner: Thu-Thao Havan

Serial No.:

09/672,954

Docket: 884.895US1

Filed:

September 29, 2000

Customer No.: 21186

September 23, 2000

186 Confirmation No.: 2928

Title:

EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE

CONTEXTS

COMMUNICATION RE: SUBMISSION OF FORMAL DRAWINGS

MAILSTOP ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Please note that the enclosed <u>4</u> sheets of formal drawings for the above-identified application should have been submitted with the Issue Fee, submitted under separate cover, and a copy of which is enclosed.

The Examiner is invited to contact Applicant's Representatives, at the below-listed telephone number, if there are any questions regarding the enclosed drawings or the payment of the Issue Fee.

Respectfully submitted,

BIMAL PODDAR

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9592

Date May 18, 2005

By Unn M. M. Cuch

Ann M. McCrackin Reg. No. 42,858 AMM1:CMG:jdgw

ANN MCCRACKIN/

Signature

Name



COPY

Receipt is hereby acknowledged for the following in the United States Patent and Trademark Office:

In re application of Bimal Poddar

Title:

EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN

MULTIPLE CONTEXTS

Serial No. 09/672,954

Filed: September 29, 2000

CONTENTS: Form PTOL-85(1 pg.), check in the amount of \$1400.00 which represents the Issue Fee Payment, Comments on Statements for Reasons for Allowance (5 pgs.), Amendment under 37 .C.F.R. § 1.312(a) (8 pgs.), check in the amount of \$3.00 for extra copies and transmittal document(1 pg.).

AMM1:CMG:djk

Mailed: May

, 2005

Docket: 884.895US1

Due Date: May 18, 2005

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicante Birbai Poddar

Title: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Docket No.: 884.895US1 Filed: September 29, 2000 Examiner: Thu-Thao Havan

Customer No.: 21186

Notice of Allowance Date:

Serial No.: 09/672,954

Group Art Unit: 2672

Due Date: May 18, 2005

Confirmation No.: 2928

February 18, 2005

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

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We are transmitting herewith the attached:

X A check in the amount \$ 1400.00 to cover the Large Entity Issue Fee Payment.

 \underline{X} A check in the amount \$3.00 to cover the Extra Patent Copies Fee (1 copies).

X Issue Fee Transmittal (Form PTOL-85).

Comments on Statements for Reasons for Allowance/Interview Summary (5 pgs.)

Amendment under 37 .C.F.R. § 1.312(a) (8 pgs.)

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

Ann M. McCrackin

Reg. No. 42,858

AMM1:CMG:djk

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PART B - FEE(S) TRANSMITTAL Complete and send this form, together v. /applicable fee(s), to: Mail Mail Stop ISS. 🚽 FEE **Commissioner for Patents** P.O. Box 1450 Alexandria, Virginia 22313-1450 (703) 746-4000 or Fax INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current and address indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" maintenance fee notifications. CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) Note: A certificate of mailing can only be used for domestic mailings of Fee(s) Transmittal. This certificate cannot be used for any other accompany papers. Each additional paper, such as an assignment or formal drawing, m have its own certificate of mailing or transmission. 02/18/2005 SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the Un States Postal Service with sufficient postage for first class mail in an envel addressed to the Mail Stop ISSUE FEE address above, or being facsim transmitted to the USPTO (703) 746-4000, on the date indicated below. P.O. BOX 2938 MINNEAPOLIS, MN 55402 DENNIS J. KAMPH (Signat 18. 200 APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 09/672,954 09/29/2000 Bimal Poddar 219.38696X00 2928 TITLE OF INVENTION: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTENTS APPLN, TYPE SMALL ENTITY ISSUE FEE PUBLICATION FEE TOTAL FEE(S) DUE DATE DUE nonprovisional NO \$1400 \$1400 05/18/2005 **EXAMINER** ART UNIT CLASS-SUBCLASS WOODS, ERIC V 2672 345-430000 Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. For printing on the patent front page, list Schwegman, Lundberg, (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. Woessner & Kluth, P.A. (2) the name of a single firm (having as a member a "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is Number is required. listed, no name will be printed. 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) SANTA CLARA, CALIFORNIA INTEL CORPORATION 4a. The following fee(s) are enclosed: 4b. Payment of Fee(s): Issue Fee A check in the amount of the fee(s) is enclosed. Publication Fee (No small entity discount permitted) Payment by credit card. Form PTO-2038 is attached. Advance Order - # of Copies __ The Director is hereby authorized by charge the required fee(s), or credit any overpayment Deposit Account Number 19-0743 (enclose an extra copy of this form). 5. Change in Entity Status (from status indicated above) a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2). The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other part interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature ANN M. MCCRACKIN

Date May 18 2005

Typed or printed name ANN M. MCCRACKIN

Registration No. 42,858

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to proc an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to comp this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 14 Alexandria, Virginia 22313-1450.

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S/N 09/672,954

PATENT

IN THE UNITED STATES PACENT AND TRADEMARK OFFICE

Applicant:

Bimal Poddar

Examiner: Eric V. Woods

Serial No.:

09/672,954

Group Art Unit: 2672

Filed:

September 29, 2000

Docket: 884.895US1

Title:

EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE

CONTEXTS

Assignee:

Intel Corporation

Customer No: 21186

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450



COMMENTS ON STATEMENTS FOR REASONS FOR ALLOWANCE AND APPLICANT INTERVIEW SUMMARIES

In response to the Notice of Allowance and Notice of Allowability mailed February 18, 2005, please consider the remarks as follows:

APPLICANT INTERVIEW SUMMARIES

Applicant would like to thank Examiner Eric Woods for initiating a telephonic interview with Applicant's Representative, Andrew DeLizio, on February 2, 2005 and on April 27, 2005 regarding the above-identified patent application.

In the February 2, 2005 interview, the Examiner proposed incorporating features of dependent claims 2, 6, 10, 14, 18, and 20 into claims 1, 5, 9, 13, 17, and 19, respectively. The Examiner indicated that these amended claims would be allowable over the cited documents of record. Applicant accepted the Examiner's proposal and authorized the Examiner to enter an Examiner's amendment as per the proposal (see Examiner's Amendment at page 2 of Notice of Allowance).

No exhibits were shown and no demonstrations were conducted. No other pertinent matters were discussed.

In the April 27, 2005 interview, the Examiner proposed amending claim 4 to correct an error in claim dependency. Specifically, the Examiner proposed amending claim 4 to depend on claim 1.

COMMENTS ON STATEMENTS FOR REASONS FOR ALLOWANCE AND INTERVIEW SUMMARIES

Serial Number: 09/672,954

Filing Date: September 29, 2000

Title: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Assignee: Intel Corporation

Page 2 Dkt: 884.895US1 (INTEL)



COMMENTS ON STATEMENTS FOR REASONS FOR ALLOWANCE

In response to the Examiner's proposal, Applicant amended the independent claims to include features recited in dependent claims (e.g., claim 1 was amended to include features of claim 2). Although Applicant amended certain claims, Applicant believes the originally filed claims are patentable over the prior art and reserves all rights for pursuing the originally filed claims in a continuation application.

The Examiner's Statement of Reasons for Allowance asserts that the combination of US Patent 6,243,736 to Diepstraten et. al, Lazzarotto's "Bitwise Logical Operations in CA-Visual Objects", and US Patent 5,818,469 to Lawless would have rendered the originally filed claims obvious. See Notice of Allowance at page 5, paragraph 24. Because the combination of Diepstraten, Lazzarotto, and Lawless was first asserted in the Examiner's Statement of Reasons for Allowance, Applicant will discuss the combination below.

Applicant respectfully traverses the Examiner's position because the cited combination does not make a *prima facia* case of obviousness.

In order for the Examiner to establish a *prima facie* case of obviousness, three base criteria must be met. First, there must be some suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine reference teachings. Second, there must be a reasonable expectation of success. Finally, the prior art reference (or references when combined) must teach or suggest all the claim limitations. The teaching or suggestion to make the claimed combination and the reasonable expectation of success must both be found in the prior art, and not based on applicant's disclosure. *M.P.E.P.* § 2142 (citing *In re Vaeck*, 947 F.2d 488, 20 USPQ2d 1438 (Fed.Cir. 1991)).

Applicant respectfully submits that the Examiner's Statement of Reasons for Allowance does not establish a *prima facie* case of obviousness because the cited documents do not teach or suggest all the claim elements, and even if they did, there is no suggestion or motivation to combine the documents.

THE REFERENCES DO NOT TEACH OR SUGGEST ALL THE CLAIM ELEMENTS

Claim 1, as originally filed, recites "obtaining a texture usage mask of a subject texture", "obtaining an inverted context ID of a subject context", and "ANDing the texture usage mask of the subject texture with the inverted context ID of the subject context to produce a resultant

COMMENTS ON STATEMENTS FOR REASONS FOR ALLOWANCE AND INTERVIEW SUMMARIES

Serial Number: 09/672,954

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Assignee: Intel Corporation



value". As discussed in Applicant's Appeal Brief, Lawless and/or Grossman (U.S. 5,230,039) do not teach or suggest these and other features of originally filed claim 1. See Appeal Brief at pp. 7-10. The only way for the combination of Diepstraten, Lazzarotto, and Lawless to teach or suggest each and every element of originally filed claim 1 is for Diepstraten and Lazzarotto to provide what is missing from Lawless. While Diepstraten discusses a "context controller for managing multitasking in a processor" and Lazzarotto discusses bitwise logical operations, the Examiner has not identified a single passage in either reference that teaches or suggests any of the features recited in originally filed claim 1. Originally filed claims 5, 9, 13, 17, and 19 include features similar to those discussed above vis-à-vis originally filed claim 1. As such, Applicant submits that the combination of Diepstraten, Lazzarotto, and Lawless does not teach or suggest each and every element of claims 1, 5, 13, 17, and 19, as originally filed.

Each of originally filed claims 2-4, 6-8, 10-12, 14-16, and 18 depend directly or indirectly on one of originally filed independent claims 1, 5, 9, 13 or 17. As a result, Applicant respectfully submits the cited combination does not teach or suggest each and every element of claims 2-4, 6-8, 10-12, 14-16, and 18.

THERE IS NO SUGGESTION TO COMBINE DIEPSTRATEN, LAZZAROTTO, AND LAWLESS

Applicant respectfully submits the Examiner has improperly combined Diepstraten, Lazzarotto, and Lawless. For a proper combination, the Examiner must show that some objective teaching in the prior art or some knowledge generally available to one of ordinary skill in the art would lead an individual to combine the relevant teaching of the references. *In re Fine*, F.2d 1071, 1074, 5 USPW2d 1596, 1598 (Fed. Cir. 1988).

The *Fine* court stated,

Obviousness is tested by "what the combined teaching of the references would have suggested to those of ordinary skill in the art." In re Keller, 642 F.2d 413, 425, 208 USPQ 871, 878 (CCPA 1981)). But it "cannot be established by combining the teachings of the prior art to produce the claimed invention, absent some teaching or suggestion supporting the combination." ACS Hosp. Sys., 732 F.2d at 1577, 221 USPQ at 933. And "teachings of references can be combined only if there is some suggestion or incentive to do so." Id. (emphasis in original).

The Examiner does not identify any passage in the references that teaches or suggests the cited combination. Because the Examiner has not identified any teaching or suggestion to combine the

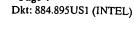
COMMENTS ON STATEMENTS FOR REASONS FOR ALLOWANCE AND INTERVIEW SUMMARIES

Serial Number: 09/672,954

Filing Date: September 29, 2000
Title: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Assignee: Intel Corporation

cited references, Applicant submits the combination is improper.



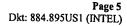


COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE AND INTERVIEW SUMMARIES

Serial Number: 09/672,954 Filing Date: September 29, 2000

Title: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Assignee: Intel Corporation





REMARKS

Claims 1, 3-5, 7-9, 11-13, 15-17, 19, 21 and 25-27 were indicated as being allowed.

The Examiner is invited to telephone Applicant's attorney, Andrew DeLizio at (281)-213-8980, or Applicant's below-named representative if there are any questions concerning this matter.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

BIMAL PODDAR

By his/her Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Attorneys for Intel Corporation

P.O. Box 2938

Minneapolis, Minnesota 55402

(612) 349-9592

Date May 18, 2005

Ann M. McCrackin

Reg. No. 42,858

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<u>S/N 09/672,954</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

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AMENDMENT UNDER 37 C.F.R. § 1.312(a) AND REQUEST FOR SUPPLEMENTAL NOTICE OF ALLOWABILITY

In response to the Notice of Allowance and Notice of Allowability mailed February 18, 2005, please amend the above-identified application and consider the remarks as follows:

Clarification of the Allowed Claims/Request for Supplemental Notice of Allowability

The Notice of Allowability indicates that the allowed claims are claims 1-21. However, Applicant respectfully points out that claims 1, 3-5, 7-9, 11-13, 15-17, 19, 21 and 25-27 were canceled by Examiner's amendment.

Accordingly, Applicant requests that the Examiner issue a supplemental Notice of Allowability that correctly lists the allowed claims as 1, 3-5, 7-9, 11-13, 15-17, 19, 21 and 25-27.

AMENDMENT UNDER 37 C.F.R. § 1.312(a) AND REQUEST FOR SUPPLEMENTAL NOTICE OF ALLOWABILITY Dkt: 884.895US1 (INTEL)

Serial Number: 09/672,954

Filing Date: September 29, 2000

Title: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Assignee: Intel Corporation



IN THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) A method comprising: obtaining a texture usage mask of a subject texture; obtaining an inverted context ID of a subject context;

ANDing the texture usage mask of the subject texture with the inverted context ID of the subject context to produce a resultant value; [[and]]

detecting that the subject texture is not being shared by another context with the subject context upon the resultant value being equal to 0 and detecting that the subject texture is being shared by another context with the subject context upon the resultant value not being equal to 0; and

revising the texture usage mask of a subject texture prior to the subject texture being used by another context by bitwise ORing the texture usage mask with a context ID of the another context to produce a resultant new texture usage mask for the subject texture.

- 2. (Canceled)
- 3. (Original) The method of claim 1, further comprising:

revising the texture usage mask of a subject texture upon the subject texture no longer being used by a particular context by deleting a context ID of the particular context from the texture usage mask to produce a resultant new texture usage mask for the subject texture.

4. (Currently Amended) The method of claim [[2]] 1, further comprising:

revising the texture usage mask of a subject texture upon the subject texture no longer being used by a particular context by deleting a context ID of the particular context from the texture usage mask to produce a resultant new texture usage mask for the subject texture.

AMENDMENT UNDER 37 C.F.R. § 1.312(a).AND REQUEST FOR SUPPLEMENTAL NOTICE OF ALLOWABILITY Page 3 Dkt: 884.895US1 (INTEL)

Serial Number: 09/672,954

Filing Date: September 29, 2000

Title: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Assignee: Intel Corporation

5. (Currently Amended) A method comprising: obtaining a texture usage mask of a subject texture; obtaining a context ID of a subject context;



performing a first logic operation with the texture usage mask of the subject texture and the context ID of the subject context to produce a resultant value; [[and]]

detecting that the subject texture is not being shared by another context with the subject context upon the resultant value being equal to a first predetermined value and detecting that the subject texture is being shared by another context upon the resultant value being equal to a second predetermined value which is different from the first predetermined value; and

revising the texture usage mask of a subject texture prior to the subject texture being used by another context by performing a second logic operation with the texture usage mask and a context ID of the another context to produce a resultant new texture usage mask for the subject texture.

- 6. (Canceled)
- 7. (Original) The method of claim 5, further comprising:

revising the texture usage mask of a subject texture upon the subject texture no longer being used by a particular context by performing a third logic operation with the texture usage mask and a context ID of the particular context to produce a resultant new texture usage mask for the subject texture.

8. (Original) The method of claim 7, further comprising:

revising the texture usage mask of a subject texture upon the subject texture no longer being used by a particular context by performing a third logic operation with the texture usage mask and a context ID of the particular context to produce a resultant new texture usage mask for the subject texture.

AMENDMENT UNDER 37 C.F.R. § 1.312(a) AND REQUEST FOR SUPPLEMENTAL NOTICE OF ALLOWABILITY Page 4 Serial Number: 09/672,954 Dkt: 884.895US1 (INTEL)

Filing Date: September 29, 2000

Title: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Assignee: Intel Corporation

9. (Currently Amended) A program storage device readable by a machine, tangibly embodying a program of instructions executable by the machine to perform a method COPA comprising:

obtaining a texture usage mask of a subject texture; obtaining an inverted context ID of a subject context;

ANDing the texture usage mask of the subject texture with the inverted context ID of the subject context to produce a resultant value; [[and]]

detecting that the subject texture is not being shared by another context with the subject context upon the resultant value being equal to 0 and detecting that the subject texture is being shared by another context with the subject context upon the resultant value not being equal to 0; and

revising the texture usage mask of a subject texture prior to the subject texture being used by another context by bitwise ORing the texture usage mask with a context ID of the another context to produce a resultant new texture usage mask for the subject texture.

- 10. (Canceled)
- (Original) The program storage device of claim 9, the method further comprising: 11. revising the texture usage mask of a subject texture upon the subject texture no longer being used by a particular context by deleting a context ID of the particular context from the texture usage mask to produce a resultant new texture usage mask for the subject texture.
- 12. (Original) The program storage device of claim 11, the method further comprising: revising the texture usage mask of a subject texture upon the subject texture no longer being used by a particular context by deleting a context ID of the particular context from the texture usage mask to produce a resultant new texture usage mask for the subject texture.
- 13. (Currently Amended) A program storage device readable by a machine, tangibly embodying a program of instructions executable by the machine to perform a method comprising:

AMENDMENT UNDER 37 C.F.R. § 1.312(a) AND REQUEST FOR SUPPLEMENTAL NOTICE OF ALLOWABILITY Page 5 Dkt: 884.895US1 (INTEL)

Serial Number: 09/672,954

Filing Date: September 29, 2000

Title: EFFICIENT DETECTION OF TEXTURE SHARING BETWEEN MULTIPLE CONTEXTS

Assignee: Intel Corporation

5.7

obtaining a texture usage mask of a subject texture; obtaining a context ID of a subject context;



performing a first logic operation with the texture usage mask of the subject texture and the context ID of the subject context to produce a resultant value; [[and]]

detecting that the subject texture is not being shared by another context with the subject context upon the resultant value being equal to a first predetermined value and detecting that the subject texture is being shared by another context upon the resultant value being equal to a second predetermined value which is different from the first predetermined value; and

revising the texture usage mask of a subject texture prior to the subject texture being used by another context by performing a second logic operation with the texture usage mask and a context ID of the another context to produce a resultant new texture usage mask for the subject texture.

- 14. (Canceled)
- 15. (Original) The program storage device of claim 13, the method further comprising: revising the texture usage mask of a subject texture upon the subject texture no longer being used by a particular context by performing a third logic operation with the texture usage mask and a context ID of the particular context to produce a resultant new texture usage mask for the subject texture.
- 16. (Original) The program storage device of claim 15, the method further comprising: revising the texture usage mask of a subject texture upon the subject texture no longer being used by a particular context by performing a third logic operation with the texture usage mask and a context ID of the particular context to produce a resultant new texture usage mask for the subject texture.
- 17. (Currently Amended) A computer-implemented method comprising: retrieving a texture usage mask of a texture; retrieving an inverted context ID of a context;

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performing a first type of logical operation of the texture usage mask of the texture with the inverted context ID of the context;

detecting whether the texture is being shared by another context with the context based on the first type of logical operation; [[and]]

performing a clear operation, an attach operation and a set operation when one of a number of texture units associated with a context completes the processing of a texture, wherein the clear operation includes clearing the identification of the context in the texture usage mask associated with the texture, the attach operation to include attaching a different texture to the one of the number of texture units that completed the processing of the texture and wherein the set operation includes setting the identification of the context in the texture usage mask for the textures being processed by the number of texture units in the context; and

and paging out a texture from a texture memory based on a type of logical operation of the identification of the contexts processing the texture and the texture usage mask of the texture, wherein the type of logical operation is the same as the first type of logical operation.

18. (Canceled)

19. (Currently Amended) A system comprising:

a number of texture units to process a number of subject textures, wherein a texture unit of the number of texture units is associated with one of a number of contexts;

- a texture memory to store at least one of the number of subject textures;
- a system memory to store at least one of the number of subject textures; [[and]]
- a processor to execute instructions that include the following operations when one of the number of texture units for a context completes the processing of a texture,

clear the identification of the context in the texture usage mask associated with the subject texture;

attach a different subject texture to the one of the number of texture units that completed the processing of the texture; [[and]]

set the identification of the context in the texture usage mask for the subject textures being processed by the number of texture units in the context; and

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a texture manager to page out a texture from the texture memory to the system memory based on a logical operation of an identification of the contexts to process the texture and the texture usage mask of the texture.

- 20. (Canceled)
- 21. (Previously Presented) The system of claim 19, wherein the logical operation is an AND operation.

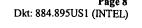
Claims 22-24. (Canceled)

- 25. (Previously Presented) The method of claim 17, wherein the first type of logical operation is an "AND" operation.
- 26. (Previously Presented) The method of claim 17, wherein the inverted context ID is a single bit in an integer word.
- 27. (Previously Presented) The method of claim 19, wherein the identification of a context is represented in a single bit in an integer word.

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REMARKS

Claims 1, 5, 9, 13, 17 and 19 are amended in order to correct unnecessary occurrences of the word "and." Claim 2 is amended to correct the claim dependency. Claims 1, 3-5, 7-9, 11-13, 15-17, 19, 21 and 25-27 remain pending.

It is respectfully submitted that these changes do not introduce new matter, and the claims are allowable without further search or consideration. Therefore, entry is appropriate under Rule 312, and is respectfully requested.

Respectfully submitted,

BIMAL PODDAR

By his/her Representatives,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 48 day of May, 2005.

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